



Form 1449 <b>INFORMATION DISCLOSURE STATEMENT</b> <b>IN AN APPLICATION</b> (Use several sheets if necessary)	Docket Number: <b>1020-017US01</b>	Application Number: <b>10/552,129</b>
	Applicant: <b>Artur W. Suntken</b>	
	Filing Date: <b>October 5, 2005</b>	Group Art Unit: <b>Unknown</b>
	Examiner Name: <b>Unknown</b>	

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Issue/Document Publication Date	Name	Filing Date If Appropriate
/MG/	4,465,379	08/14/1984	Misawa et al.	
/MG/	4,827,133	05/02/1989	Zierhut	
/MG/	5,461,425	10/24/1995	Fowler et al.	
/MG/	5,811,808	09/22/1998	Cannata et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Publication Date	Country	Translation	
				Yes	No

**OTHER DOCUMENTS (Including Authors, Title of Item, Page(s), Vol/Issue No., Publisher, Place of Publication)**

/MG/	H. Kogure et al., "Analysis of CMOS ADC Nonlinear Input Capacitance," IEICE Trans. Electron., Vol. E85-C, No. 5, May 2002, pp. 1182-1190.
/MG/	"Theory of Operation Multi-Bit Enhanced Noise Shaping," Burr-Brown Integrated Circuits Data Book – Data Conversion Products, XP-002303870, p. 2.341, 1994.

EXAMINER	/Mark Gaworecki/	Date Considered
		10/24/2007

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Based on Form PTO-FB-A820  
(Also form PTO-1449)

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